



09/517127

CONF

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Moore et al.

Examiner: Timothy V Eley

Patent No.: 7,530,877

Group Art Unit: 3724

Issue Date: May 12, 2009

Docket No: 1998-1234.00/US

Title: SEMICONDUCTOR PROCESSOR SYSTEMS, A SYSTEM CONFIGURED TO PROVIDE A
SEMICONDUCTOR WORKPIECE PROCESS FLUID

Commissioner for Patents
Office of Patent Publication
ATTN: Certificate of Correction Branch

We are transmitting herewith the attached:

- ☒ Request for Certificate of Correction.
- ☒ Certificate of Correction Form - PTO-1050 (in duplicate)
- ☒ A return postcard.

Please charge any additional fees or credit overpayment to Deposit Account No. 13-3092.

Customer Number: 26809

Respectfully submitted,

//Richard Henkler//

Date: June 17, 2009


By: _____
Rich Henkler
Reg. No. 39,220

Certificate
JUN 24 2009
of Correction

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Commissioner of Patents and Trademarks, P.O. Box 1450, Alexandria VA 22313-1450, on this 17 day of June 2009.

Jenny Mundt

Name



Signature



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REQUEST FOR CERTIFICATION OF CORRECTION

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450
ATTN: Certificate of Correction Branch

It is requested that a Certificate of Correction be issued correcting printing errors appearing in the above-identified United States patent. Two copies of the text of the Certificate in the suggested form are enclosed.

As the error is that of the Patent Office, it is believed that no fee is due.

Issuance of the Certificate of Correction would neither expand nor contract the scope of the claims as properly allowed, and re-examination is not required.

Respectfully submitted,

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(Also Form PTO-1050)

UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO : 7,530,877

Page (1) of 1

DATED : May 12, 2009

INVENTOR(S) : Moore et al.

It is certified that errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In column 27, lines 8-9, in Claim 46, delete "semiconductorworkpiece" and insert - - semiconductor workpiece - -, therefor.

In column 27, line 18, in Claim 46, after "system" delete "is".

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